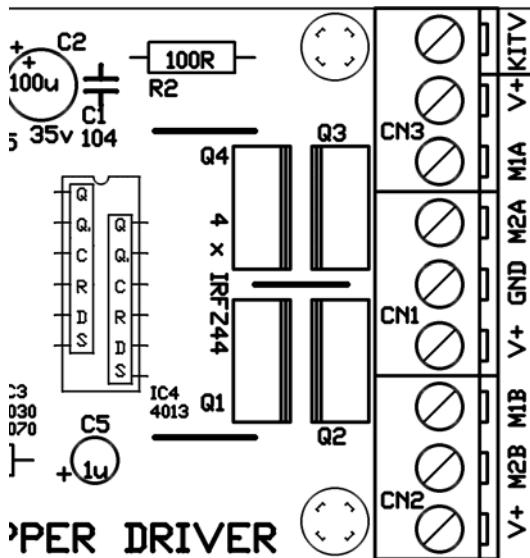


RELABELLING OF OUTPUT TERMINALS Kit 179



Please note we made some errors on the labelling of the output terminals on this PCB.

The correct labelling is shown to the left.

Two errors in the schematic (all on the 4030) have now been corrected in the documentation.